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				First Named Inventor	Kevin Harer .	
				Art Unit	2123	
(Use as many sheets as necessary)			icusury)	Examiner Name	T. Stevens	
Sheet	1	of	1	Attorney Docket Number	06816.0173	

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THE		M. Kantrowitz and L. Noack. I'm Done Simulating: Now what? Verification Coverage Analysis and Correctness Checking of the DECchip 21164 Alpha microprocessor. Proceedings of 33rd DAC, 1996. pp. 325-330.	
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